## 100V N-CHANNEL ENHANCEMENT MODE MOSFET

## **SUMMARY**

 $V_{(BR)DSS} = 100V$ ;  $R_{DS(ON)} = 0.230\Omega$   $I_D = 1.9A$ 

#### **DESCRIPTION**

This new generation of TRENCH MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



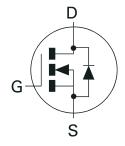
SOT23-6

## **FEATURES**

- Low on-resistance
- · Fast switching speed
- · Low threshold
- · Low gate drive
- SOT23-6 package

## **APPLICATIONS**

- DC DC Converters
- Power Management Functions
- Disconnect switches
- Motor control

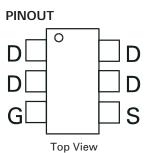


## **ORDERING INFORMATION**

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMN10B08E6TA	7″	8mm	3000 units
ZXMN10B08E6TC	13"	8mm	10000 units

## **DEVICE MARKING**

• 10B8





## ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	100	V
Gate Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current $V_{GS}=10V$ ; $T_A=25^{\circ}C$ (b) $V_{GS}=10V$ ; $T_A=70^{\circ}C$ (b) $V_{GS}=10V$ ; $T_A=25^{\circ}C$ (a)	ID	1.9 1.5 1.6	А
Pulsed Drain Current (c)	I <sub>DM</sub>	9	А
Continuous Source Current (Body Diode) (b)	IS	2.5	А
Pulsed Source Current (Body Diode) (c)	Ism	9	А
Power Dissipation at T <sub>A</sub> =25°C (a) Linear Derating Factor	PD	1.1 8.8	W mW/°C
Power Dissipation at T <sub>A</sub> =25°C (b) Linear Derating Factor	PD	1.7 13.6	W mW/°C
Operating and Storage Temperature Range	T <sub>j</sub> :T <sub>stg</sub>	-55 to +150	°C

## THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT	
Junction to Ambient (a)	$R_{\theta JA}$	113	°C/W	
Junction to Ambient (b)	$R_{\theta JA}$	73	°C/W	

#### NOTES

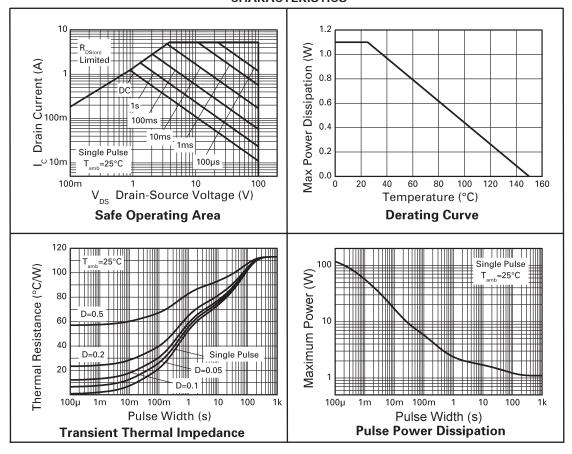
(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions

(c) Repetitive rating 25mm x 25mm FR4 PCB, D = 0.02, pulse width  $300\mu s$  - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph



<sup>(</sup>b) For a device surface mounted on FR4 PCB measured at t  $\! \leqslant \! 5$  secs.

## **CHARACTERISTICS**





# ELECTRICAL CHARACTERISTICS (at $T_A = 25^{\circ}\text{C}$ unless otherwise stated).

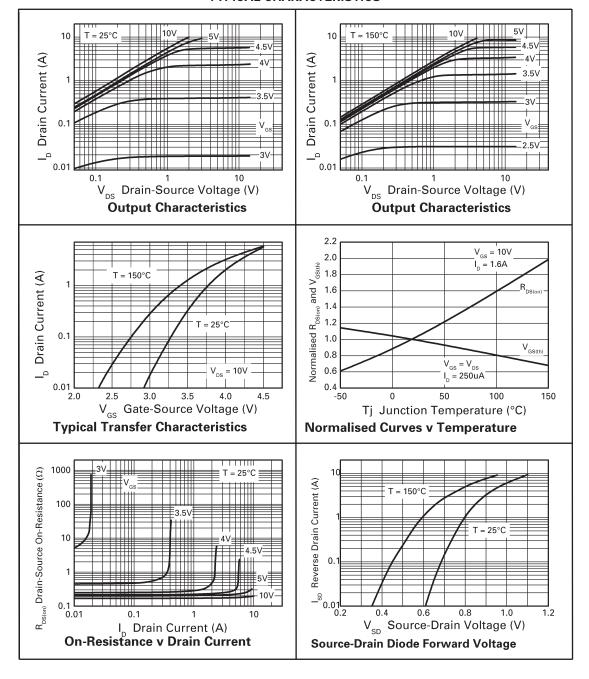
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.		
STATIC	•		•		•			
Drain-Source Breakdown Voltage	V(BR)DSS	100			V	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			0.5	μΑ	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V		
Gate-Body Leakage	I <sub>GSS</sub>			100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V		
Gate-Source Threshold Voltage	VGS(th)	1.0		3.0	V	I <sub>D</sub> =250μA, V <sub>DS</sub> = V <sub>GS</sub>		
Static Drain-Source On-State Resistance (1)	R <sub>DS(on)</sub>			0.230 0.300 0.500	Ω	V <sub>GS</sub> =10V, I <sub>D</sub> =1.6A V <sub>GS</sub> =4.5V, I <sub>D</sub> =1.4A V <sub>GS</sub> =4.3V, I <sub>D</sub> =1.1A		
Forward Transconductance (1)(3)	9fs		4.8		S	V <sub>DS</sub> =15V,I <sub>D</sub> =1.6A		
DYNAMIC (3)								
Input Capacitance	C <sub>iss</sub>		497		pF			
Output Capacitance	Coss		29		pF	V <sub>DS</sub> =50 V, V <sub>GS</sub> =0V, f=1MHz		
Reverse Transfer Capacitance	C <sub>rss</sub>		18		pF			
SWITCHING(2) (3)								
Turn-On Delay Time	t <sub>d(on)</sub>		2.9		ns	$V_{DD} = 50V, I_{D} = 1.0A$ $R_{G} = 6.0\Omega, V_{GS} = 10V$		
Rise Time	t <sub>r</sub>		2.1		ns			
Turn-Off Delay Time	t <sub>d(off)</sub>		12.1		ns			
Fall Time	tf		5.0		ns			
Gate Charge	$Q_g$		5.0		nC	V <sub>DS</sub> =50V,V <sub>GS</sub> =5V, I <sub>D</sub> =1.6A		
Total Gate Charge	Qg		9.2		nC			
Gate-Source Charge	Ogs		1.7		nC	V <sub>DS</sub> =50V,V <sub>GS</sub> =10V, I <sub>D</sub> =1.6A		
Gate-Drain Charge	Q <sub>gd</sub>		2.5		nC			
SOURCE-DRAIN DIODE								
Diode Forward Voltage (1)	V <sub>SD</sub>		0.85	0.95	V	TJ=25°C, IS=2.0A, VGS=0V		
Reverse Recovery Time (3)	t <sub>rr</sub>		32.0		ns	T <sub>J</sub> =25°C, I <sub>F</sub> =1.7A,		
Reverse Recovery Charge (3)	Orr		40.0		nC	di/dt= 100Å/μs		

#### NOTES

- (1) Measured under pulsed conditions. Width=300 $\mu$ s. Duty cycle  $\leq 2\%$  .
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.

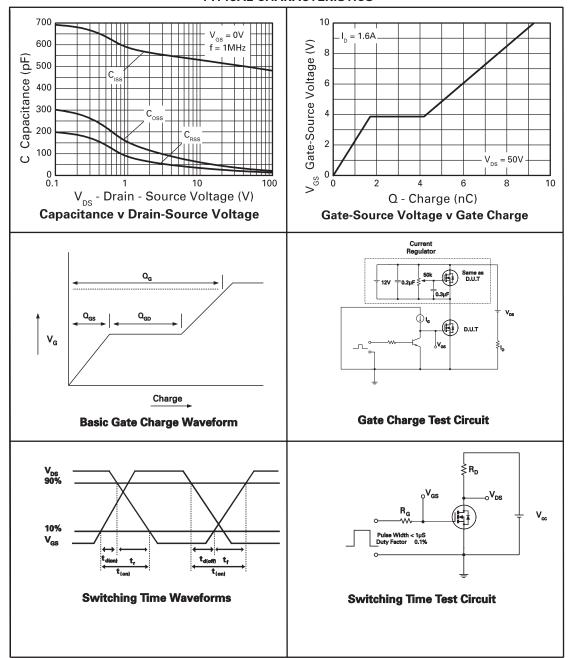


## TYPICAL CHARACTERISTICS





#### TYPICAL CHARACTERISTICS

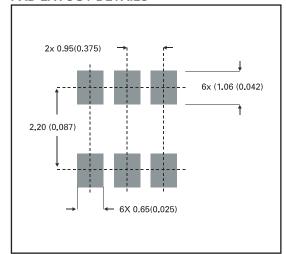




## **PACKAGE OUTLINE**

# e E EI EI DATUMA A A2 A2 A1

## PAD LAYOUT DETAILS



CONTROLLING DIMENSIONS IN MILLIMETRES APPROX CONVERSIONS INCHES.

#### **PACKAGE DIMENSIONS**

DIM	Millimetres		Inches		DIM	Millimetres		Inches	
DIIVI	Min	Max	Min	Max	DIIVI	Min	Max	Min	Max
Α	0.90	1.45	0.35	0.057	Е	2.60	3.00	0.102	0.118
A1	0.00	0.15	0	0.006	E1	1.50	1.75	0.059	0.069
A2	0.90	1.30	0.035	0.051	L	0.10	0.60	0.004	0.002
b	0.35	0.50	0.014	0.019	е	0.95 REF		0.037	REF
С	0.09	0.20	0.0035	0.008	e1	1.90 REF		0.074	REF
D	2.80	3.00	0.110	0.118	L	0°	10°	0°	10°

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